METHOD AND SYSTEM FOR DETERMINING CONSTRAINTS FOR A PRINTED CIRCUIT BOARD DESIGN MODULE

Abstract of the Disclosure

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A method for generating a printed circuit board design module is provided. The method includes compiling information that is usable to derive one of more low level details associated with a printed circuit board, determining one or more high level constraints that are usable with the information to generate the low level details, and generating the printed circuit board design module such that the printed circuit board design module includes the information and such that the printed circuit board design module is configured to receive the one or more high level constraints..